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	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01207M	

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1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
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11	Melear; "Using Background Modes for Testing, Debugging and Emulation of Microcontrollers"; IEEE 1997; 8 pages	<input type="checkbox"/>

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12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	<input type="checkbox"/>
13	Anonymous; "JEEN JTAG Embedded Ice Ethernet Interface"; August 1999; Embedded Performance, Inc.; 3 pages	<input type="checkbox"/>
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15	"Microsoft Files Summary Judgement Motions"; February 1999; Microsoft PressPass; retrieved on May 20, 2005 from http://www.microsoft.com/presspass/press/1999/feb99/Feb99/Calderapr.asp ; 3 pages	<input type="checkbox"/>
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23	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages	<input type="checkbox"/>
24	CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation;" October 10,2001; 09/975,104; Snyder; 35 pages	<input type="checkbox"/>
25	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
26	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools"; Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	<input type="checkbox"/>
27	Microsoft Press Computer User's Dictionary; 1998; 3 pages	<input type="checkbox"/>
28	CD00186; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events;" October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	<input type="checkbox"/>
29	CD00185; "Emulator Chip-Board Architecture for Interface;" October 10, 2001; 09/975,030; Snyder et al.; 37 pages	<input type="checkbox"/>
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31	Wikipedia - Processor register, retrieved on March 7, 2006 from http://en.wikipedia.org/wiki/Processor_register ; 4 pages	<input type="checkbox"/>
32	Dahl, et al.; "Emulation of the Sparcle Microprocessor with the MIT Virtual Wires Emulation System"; 1994; IEEE; 9 pages	<input type="checkbox"/>
33	Bauer et al.; "A Reconfigurable Logic Machine for Fast Event-Driven Simulation"; 1998; Design Automation Conference Proceedings; 8 pages	<input type="checkbox"/>

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	34	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages	<input type="checkbox"/>
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